

REMARKS/ARGUMENTS

Favorable reconsideration of this application as presently amended and in light of the following discussion is respectfully requested.

Claims 1-21 are pending in the present application. Claims 1 and 2 have been amended by the present amendment.

In the outstanding Office Action, Claims 1-13 were rejected under 35 U.S.C. § 103(a) as anticipated by Nakamura et al. (herein "Nakamura") in view of Aoki; and Claims 14-21 were indicated as allowed.

Applicant thanks the Examiner for the indication that Claims 14-21 are allowed.

Amended Claim 1 finds support in the drawings as originally filed, for example, Figure 5. In addition, Claim 2 has been amended for clarification. No new matter is added thereby.

Claims 1-13 stand rejected under 35 U.S.C. § 103(a) as anticipated by Nakamura and Aoki. This rejection is respectfully traversed.

Amended Claim 1 is directed to a liquid crystal display that includes *inter alia* first latch circuits that latch digital gradation data consisting of bits in different timings. The first latch circuits are provided to every multiple signal lines.

By providing such first latch circuits, an implementation area of a signal line drive circuit is reduced (see specification, page 13, lines 30-37).

On the contrary, Nakamura discloses n pieces of first latch circuits 102 for latching video signals (see column 41, lines 50-62). Moreover, as shown in Figure 9 of Nakamura, the first latch circuit 102 is provided for each video signal. Therefore, Nakamura is not possible to reduce the number of the first latch circuits 102, and is not possible to reduce the implementation area of a signal line drive circuit.

Further, as shown in Figure 1 of Aoki, the sampling circuit 106 is provided for each data signal line 112 (see also abstract). Thus, according to Aoki, it is not possible to reduce the number of the sampling circuit 106, and it is not possible to reduce the implementation area of a signal line drive circuit.

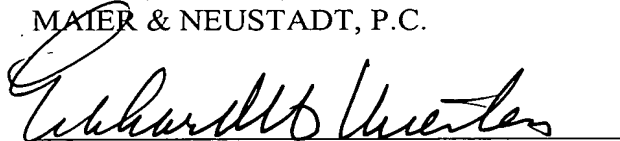
In addition, because neither Nakamura nor Aoki discloses the first latching circuits as recited in Claim 1, even the combined teachings of these cited references would not in any way render the structure recited in Claim 1 obvious.

Accordingly, it is respectfully submitted that independent Claim 1 and each of the claims depending therefrom define over the cited art.

Consequently, in light of the above discussion, and in view of the prior indication of allowed claims and the present amendment, Applicant respectfully submits that the present application is in condition for allowance, and an early action favorable to that effect is earnestly solicited.

Respectfully submitted,

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